AAV-HD-XMUX(-T/R)/ AAV-SD-XMUX(-T/R)

HD/SD analog/digital audio embedder

User manual

Rev. G

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Revision history

Current revision of this document is the uppermost in the table below.

Rev.	Repl.	Date	Sign	Change description
G	6	2015-05-14	MB	Cover page update; DoC removed; no other changes to content
6	5	2011-03-08	AA	Updated Declaration of Conformity.
5	4	2009-06-11	MDH	Added A/D gain setting table, new DIP switch routing table, 600 ohm backplane option.
4	3	2009-01-06	NBS	Corrected LED descriptions in Chapter 5.1.
3	2	2008-12-18	MDH	Dip switch description corrected
2	1	2008-08-26	MDH	Video input manual mode described. Embedding core diagram changed. LED chart corrected.
1	0	2008-07-11	NBS	Updated formats to company standard
0	-	2007-06-05	MDH	First revision derived from AV-HD-XMUX manual rev3

Contents

1 Product overview

> The only difference between the AAV-SD-XMUX and the AAV-HD-XMUX is that the latter can also handle HD SDI video.
>
> The rest of the manual will only refer to the AAV-HD-XMUX.

The AAV-HD-XMUX is a highly integrated audio embedding module in the Flashlink range, offering simultaneous embedding and de-embedding of audio from a digital HD or SD serial video signal.

The modules can:

- AAV-HD-XMUX can handle SD and HD digital uncompressed video.
- AAV-SD-XMUX can handle SD digital uncompressed video.
- De-embed and embed all groups of audio.
- Copy or move audio groups without additional delay.
- De-embed 2 AES3 digital audio and non-audio signals.
- Embed 2 AES3 digital audio and non-audio signals.
- Embed 4 analog audio signals.
- Apply sample rate converters when needed on the AES3 inputs.
- Apply extra audio delay.
- Swap stereo channels.
- Make mono or sum from stereo signals.
- Have optical laser output.
- Have optical input.
- Transport asynchronous serial data.
- Generate video and audio signals.
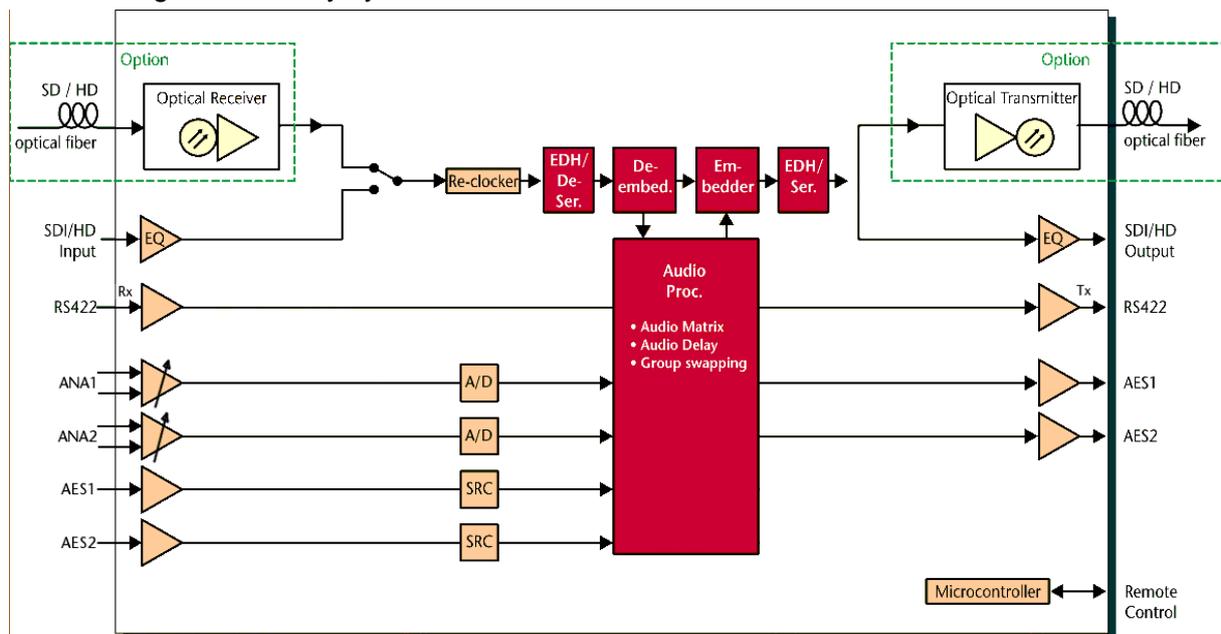- De-glitch correctly synchronized switched video.

Figure 1: Module overview

The module has two main processing blocks. One processes the video stream and the packet data, the other processes the audio. The packet processing core forms a group router which can route embedded audio between groups without any extra delay.

The AAV-HD-XMUX audio core is an AES3 stereo audio router. The received embedded audio and the audio inputs are the sources in the router. The embedded output groups and AES3 outputs are the destinations. This feature may also be used to perform stereo channel swapping.

Four stereo delay lines are also available in the router with a total combined delay of 1.25s.

Audio processing is possible within each stereo output. The channels may be changed allowing L/R swapping, mono assignment, summing, MS conversion and phase reversal of one of the signals.

There are two embedding modes:

-   Non-SRC mode: The AES3 input signals are embedded transparently.
-   Automatic mode: The module will use sample rate converters when necessary.

Data signals such as Dolby E will always be embedded transparently without using the sample rate converters.

All embedding and de-embedding is performed with synchronous 48 kHz audio.

The unit may be ordered with optional optical transmitters and receivers. The laser options range from the standard -7.5dBm 1310nm to the CWDM units. The receivers may be either HD single mode or HD multi mode. Both units will receive both HD and SD data rates. The module has signal generators for audio and video for test and line up applications. The internal video generator may be used as a fall-back source that is used if the both the electrical and the optical input signals fail. This allows uninterrupted transmission of embedded audio. The user may also configure the module to mute the outputs if the input signal disappears.
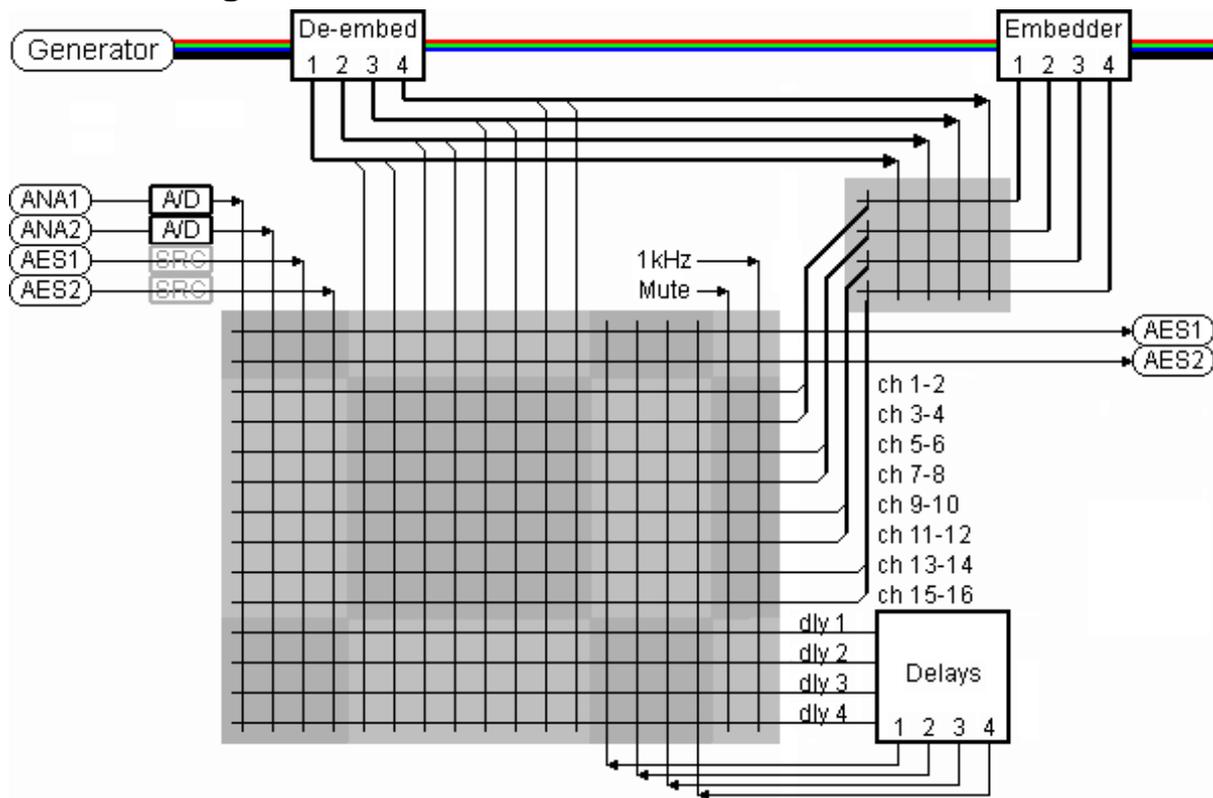
1.1 Audio signal flow

Figure 2: Processing core overview

1.2 Signal flow

Video may be presented on the optical or electrical inputs. The module will switch to the other input if the module can not lock to a signal. The video is re-clocked and transformed to parallel video. The parallel video goes into a line buffer which is used to de-glitch the video when switched on the correct line. No errors are flagged or produced when the video is switched on the appropriate switching line. All ancillary data, including embedded audio is extracted from the video signal. All the packed data is sent to the group router. The de-embedded audio is sent to the stereo audio cross-point router.

The analog inputs (Stereo inputs 1 & 2) are always connected directly to the audio router.

The AES audio inputs (Inputs 3 & 4) are initially connected directly to the audio router. Sample rate converters are inserted if there are sample slips but only if the signals are *not* data signals. The sample rate converters may be disabled with DIP switch 1.7.

The audio processing is performed on the stereo router outputs.

Four of the router outputs are connected to the four stereo delay lines. The outputs of the delay lines are connected to four inputs of the stereo audio router.

The audio signal is delayed by a few samples during de-embedding, re-packeting the audio and audio processing. Signals that pass through the stereo audio router will be delayed by a small number of samples.

The *group router* outputs from the de-embedders do not introduce any additional delay as the audio does not require unpacking and re-packing.

The embedder core embeds either re-packeted audio from the stereo router or the existing de-embedded audio as configured in the *group router*.

The embedded audio packets are inserted into the video signal together with the control packets and any other packets that were present in the original video signal. The video is serialized and output through the cable and laser drivers. The audio signals are taken from the outputs of the audio router.

1.3 Data signal

The data signal is transported using the User bits in one of the embedded audio streams. De-embedded data is output on the RS485 output. Data received at the R422 input is embedded into the output video. The configuration sets the audio source containing the data signal to de-embed, the data format to be received on the backplane connector and which audio output signal to embed data into.

2 Specifications

2.1 Measurement conditions

Audio Sampling rate: 48 kHz.
Ambient temperature: 25ºC.

2.2 General

Power: +5V DC 0.7A 3.5W
                                    +/-15V DC 0.017A 0.5W
Control: DIP switches, GYDA system controller.
Monitoring: Front panel LEDs, GYDA system controller and GPI.
EDH/CRC processing: Full. Received flags are updated, new CRCs are
                                    calculated.
Boot time: 1.2 second.
Digital audio inputs and outputs: Conform to AES3-2003.
Video inputs and outputs: Conform to SMPTE 292M-1998.
Data input and output: Conform to EIA RS-485.

2.3 Processing

Video latency is variable due to the de-glitcher but the values below apply when the video signal is first applied. Other latency values are maximum values.

2.3.1 SD latencies

Video: des+4+350+256+2+ser video samples = 45.3us.
Audio embedding: AES+2+1+16 audio samples=20/48000 = 417us.
Audio de-embedding: 4+16+1 audio samples =21/48000 = 438us.
Embedding data GPI mode: 8+4+32 96kHz samples =44/96000 = 458us.
Embedding data UART mode: 32+128+17+16 96kHz samples =193/96000 = 2.01ms.
De-embedding data GPI mode: 8+32+8 96kHz samples =44/96000 = 458us.
De-embedding UART mode: 8+32+8 96kHz samples =48/96000 = 458us.

2.3.2 HD latencies

Video: des+8+1024+1024+3+ser video samples = 27.6us.
Audio embedding: AES+2+1+8=12 /48000 = 250us.
Audio de-embedding: 2+8+1=11/48000 = 229us.
Embedding data GPI mode: 8+4+16 96kHz samples =28/96000 = 292us.
Embedding data UART mode: 16+128+17+16 96kHz samples =177/96000 = 1.84ms.
De-embedding data GPI mode: 4+16+8 96kHz samples =28/96000 = 292us.
De-embedding UART mode: 4+16+8 96kHz samples =28/96000 = 292us.

2.4 Inputs

2.4.1 Electrical video input
AAV-HD-XMUX

Video Data rate: 270Mbps or 1,485Gbps.
Video frame rate: 24p, 50i, 60i, 50p or 60p and pull down rates
                                    PsF formats are handled as interlaced formats.

AAV-SD-XMUX

Video Data rate: 270Mbps.
Video frame rate: 50i and pull down 60i rates.
Equalization: Automatic up to 35dB.
Impedance: 75 ohm.

Return loss: >15dB up to 1,5GHz.
Signal level: nom. 800mV.
Connector: BNC.

2.4.2 Optical video input
Optical wavelength: 1200-1620nm ±40 nm.
Maximum Optical power: 0 dBm HD PIN.
Minimum Optical power HD/ SD: -22/-26 dBm with HD PIN.
Return loss: Better than 27 dB.
Maximum reflected power: 4%.
Transmission circuit fiber: Single mode HD PIN/ (Multi-mode option on request);
                                      Multi-mode PIN is standard with AAV-SD-XMUX.
Connector: SC/UPC.

2.4.3 Analog audio inputs
Number of inputs: 4.
Sampling frequency: 48 kHz.
Differential input impedance: 24 kohm.
Common mode input impedance: 50 kohm.
Connector (C1 backplanes):   DB-25F (Female with UNC threads).
Maximum signal level (0 dBFS):   +12, +13.5, +15, +16.7, +18, +20, +21 or +24dBu.
Level precision: +/- 0.1dB where Zsource < 40 ohm.
Common mode voltage tolerance: +/- 30V – (Maximum peak signal level).
Frequency response: 20 Hz – 20 kHz 0.1 dB.
Pass-band ripple: +/- 0.005 dB.
Stop band attenuation: 80 dB.
Dynamic range[1]:   Min. 103 dB (A), typical 107 dB (A).
THD+N @ -1 dBFS: Max. -90 dB, typical -96 dB.
Intermodulation distortion[2]:   Max. -90 dB, typical -95 dB.
Crosstalk: Max. -90 dB, typical -99 dB.
CMRR (20 Hz – 15 kHz):   Max. 70 dB @ 15 kHz, typical -90 dB @ low
                                      frequencies.

2.4.4 Digital Audio Inputs
Number of AES3 inputs: 2.
Audio data rate: 30 kHz to 200 kHz, converted to 48 kHz if not 48 kHz
                                      and isochronous to the video input signal.
Input impedance: 110 ohms transformer balanced.
Connector (C1 backplanes):   DB-25F (Female with UNC threads).
Embedded audio word length: 20 or 24 bits.
Embedded audio Channel Status: As received when isochronous, otherwise fixed.
Sample rate converter dynamic   139 dB (A) @ 1kHz.
range:

2.4.5 Data inputs
RS422:   1.
Connector: 8P8C Modular jack.
UART mode:
Baud rates: 9600 to 115200.
Data length: 7 or 8 bits.
Parity: None, odd or even.
Stop bits: 1, 1.5 or 2 bits.

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[1] |THD+N of -60 dBFS 1 kHz signal| + 60.
[2] Signal at -12 dBFS, SMPTE 4:1 60 Hz + 7 kHz.

GPI mode:
Raw data sampling frequency: 93750 Hz.

2.5 Outputs

2.5.1 Electrical video output
Number of HD/SDI outputs: 1.
Connector: BNC.
Impedance: 75 ohm.
Return loss: > 15 dB to1.5GHz.
Signal level: nom. 800mV.
Rise/fall time: typically 650ps. @270Mbps. < 270ps @1.485Gbps.

2.5.2 Optical video output
Optical wavelength (13T):   1310nm ±40nm.
Light source: FP semiconductor laser.
Optical power: -7.5 dBm.
Extinction ratio: >5:1.

Optical wavelengths (13T0, 15T,   1270, 1290, 1310, 1330, 1350, 1370, 1390, 1410,
CxxxxT):   1470, 1490, 1510, 1530, 1550, 1570, 1590, 1610nm
                                      ±6nm.
Light source: DFB semiconductor laser.
Optical power: 0 dBm.
Extinction ratio: >10:1.
Jitter (UI=unit interval):   <0.135 UI @ SD, <0.2 UI @ HD.
Return loss: better than 40 dB typ.
Maximum reflected power: 4%.
Transmission circuit fiber: 9/125um Single Mode.
Connector: SC/UPC.

2.5.3 Digital Audio outputs
Number of AES3 outputs: 2.
Audio data rate: 48 kHz.
Impedance (C1 backplanes):   110 ohm transformer balanced.
Connector (C1 backplanes):   DB-25F (Female with UNC threads).

2.5.4 Data outputs
Number of RS485 outputs   1.
Connector   8P8C Modular jack.

2.5.5 GPI outputs
Signals: Power status good, no video input lock, laser failure.
Connector: 8P8C Modular jack.
Signal type: Open drain transistor with free-wheel diode.
Maximum voltage: 100 V.
Maximum current: 150 mA.

3 Configuration

The XMUX embedding core can be considered as an 18x14 stereo audio router and a 5x4 group router.

The group router is used to transport or shuffle groups without introducing any additional delay.

The inputs or sources in the stereo router are from the de-embedded audio groups, the AES and analog audio inputs, the delay line outputs and the two built in generators.

The stereo router outputs or destinations are the groups of embedded audio in the output video, the AES outputs and the delay line inputs.

A normal de-embedder configuration would route the de-embedded audio to the AES outputs.

A normal embedder configuration would be to route the audio inputs to the appropriate embedder group outputs.

The AAV-HD-XMUX module can do both at the same time!

Many other configurations are possible and the module may be dynamically controlled as an 18x14 audio router via the system controller, GYDA.

Full control of the module is performed with the GYDA system controller. Controls only possible with GYDA are:

- The data transmission parameters and channel selection.
- The output processing of each stereo signal (LR, RL, LL, RR, MS, Sum, ØLR, LØR).
- The delay lines delays and routing.
- Video and audio generator configuration.

> **ATTENTION:** The switch settings are only read when the module is powered up.

3.1 DIP switch Configuration

Full hardware control of all of the parameters in the module would require either, a complicated menu type of control interface with a display and control buttons; or an enormous number of switches. In many cases, most of the parameters will not be changed from the default settings. It was decided to control only the most used parameters with switches. This still requires the use of 24 switches.

The switches are only read if SW1.8 (DIP configuration mode) is in the on position.

There are not enough switches on the module to allow full stereo routing configurations. Groups of four channels are routed together as units, for example: AES input channels 1&2, embedded audio group 1.

3.1.1 Destinations

The switches control the routing of signals to the outputs or destinations. There are four embedded audio groups and one pair of AES outputs. The configuration assigns sources to output groups and the pair of AES outputs. This allows the same input signals to be routed to several outputs.

There is a group of three switches for each of the outputs. The combination of the three switches set the input source or disables the output e.g.

- Group 1 embedded output is controlled by switches on SW1 positions 1, 2 and 3.
- AES outputs are controlled by switches on SW3 positions 1, 2 and 3.

3.1.2 Sources

There are eight possible permutations of the switches. Seven of the permutations choose the input sources. One of the settings (off, off, off) is used to disable the group embedding or set the AES outputs to silence.

Table 1: Switch encoding

|		Outputs to Embedders				External outputs
		Group 1	**Group 2**	**Group 3**	**Group 4**	**AES1/2 Out**
		Switch 1	Switch 1	Switch 2	Switch 2	Switch 3
		1 2 3	**4 5 6**	**1 2 3**	**4 5 6**	**1 2 3**
From De-embedders	**Group 1**	0 0 1	0 0 1	0 0 1	0 0 1	0 0 1
	Group 2	0 1 0	0 1 0	0 1 0	0 1 0	0 1 0
	Group 3	0 1 1	0 1 1	0 1 1	0 1 1	0 1 1
	Group 4	1 0 0	1 0 0	1 0 0	1 0 0	1 0 0
	ADC1 /2	1 0 1	1 0 1	1 0 1	1 0 1	1 0 1
	AES1 /2	1 1 0	1 1 0	1 1 0	1 1 0	1 1 0
Stereo Tone		1 1 1	1 1 1	1 1 1	1 1 1	1 1 1
No Output		0 0 0	0 0 0	0 0 0	0 0 0	0 0 0

1 = on, 0 = off.

3.1.3 Examples
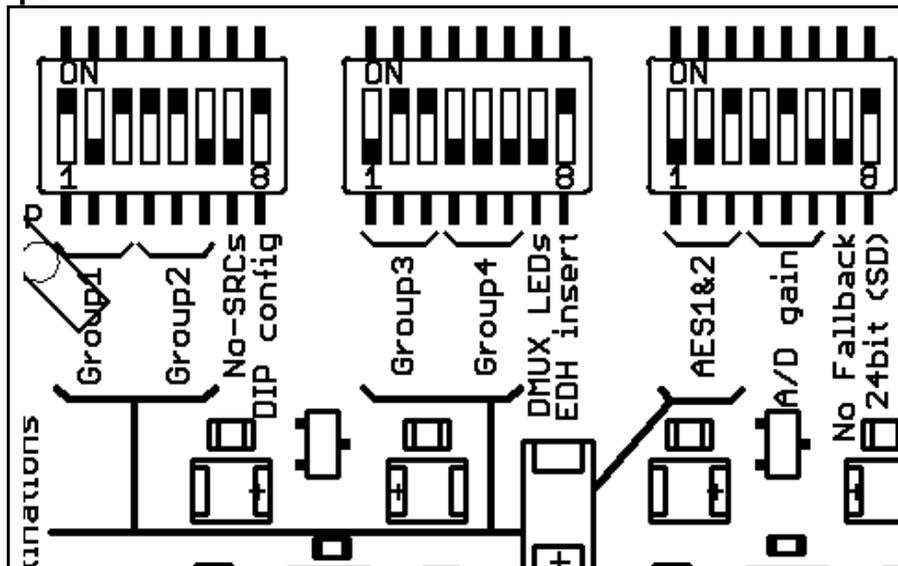
Figure 3: Example 1

The module above (Figure 1) is set to the following:

- Group1 output is embedded with signals from the ADC inputs
- Group2 output is embedded with signals from AES1&2 inputs
- Group3 output is embedded with signals from de-embedded group3
- Group4 output is not embedded
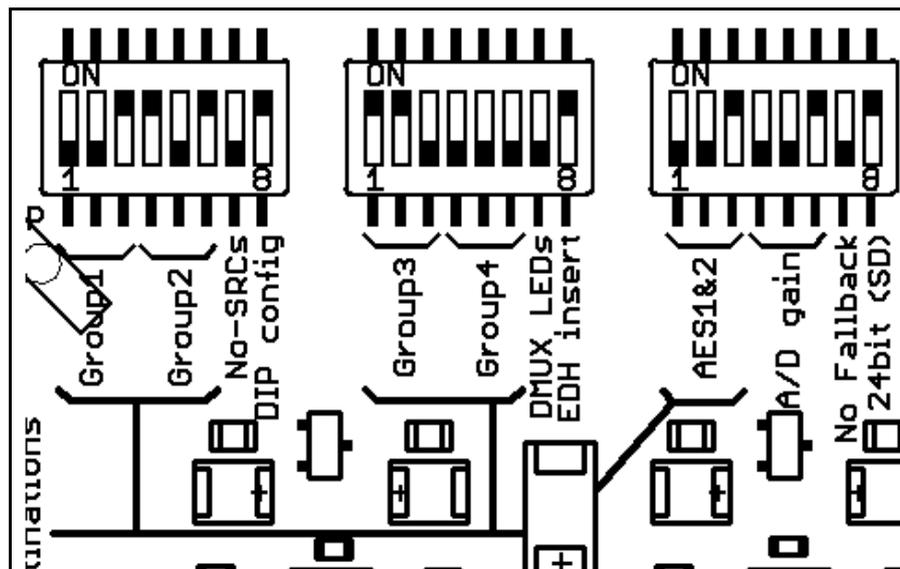- AES1&2 outputs signals from de-embedded group1

Figure 4: Example 2

The module above (Figure 4) is set to the following:

- Group1 output is embedded with signals from de-embedded group1
- Group2 output is embedded with signals from the ADC inputs
- Group3 output is embedded with signals from AES1&2 inputs
- Group4 output is not embedded
- AES1&2 outputs signals from de-embedded group1

Users familiar with binary numbers may see that numbers 1 to 4 (001 to 100) correspond to groups 1 to 4.

3.1.4 Other DIP Switches

3.1.4.1 A/D converter gain, SW3.4+5+6
The A/D input levels may be set to one of the eight preset levels. The levels correspond to the maximum sine wave level, otherwise known as 0 dBFS.

SW3-4,5,6	000	001	010	011	100	101	110	111
Max.level (dBu)	+12	+13.5	+15	+16.7	+18	+20	+21	+24

All four input levels are set by the DIP switches in DIP configuration mode. GYDA can set the levels for each stereo pair.

3.1.4.2 Non-SRC mode, SW1.7
When SW1.7 and SW1.8 are on, the sample rate converters will not be used. The user must ensure that the AES input signals are locked to the video signal audio; otherwise click noises will be produced in the embedded audio signals.

3.1.4.3 DIP Configuration, SW1.8
SW1.8 on, forces the DIP switch configuration to be used. If there is a GYDA present, the switch configuration on the module will be used but the configuration will be monitored in the GYDA controller. Control of the card parameters can not be changed with GYDA.

SW1.8 off will not use the DIP switches but will be configured from either the stored configuration in the module or from GYDA if there is GYDA present. The configuration will be stored when a GYDA configuration command is used. Therefore if a GYDA is present, the internal configuration will be overwritten by the GYDA controller.

The DIP switch settings control the routing and a couple of other important settings. Other stored settings, such as data embedding and generator settings will always be used.

3.1.4.4 LED mode, SW2.7
The switch controls how the two audio LEDs function. If the switch is off, the LEDs show the AES receiver status. If the switch is on, the LEDs show the presence of embedded groups.

3.1.4.5 EDH insert, SW2.8
SD video output from the module will only contain an EDH packet if SW2.8 is on.

3.1.4.6 Fallback generator control, SW3.7
This switch is used to control the outputs when the input signals are not present.

SW3.7 **on**: The Video output will be disabled if the input signal is removed.

The AES outputs will be disabled if the source routed to that output is not present. The input presence is embedded in the embedded audio data packet so that an AES input failure on a distant card will disable an AES output which uses audio embedded by that distant card.

SW3.7 **off**: The internal video generator will be used as an input until a valid video signal is detected on one of the inputs.

The AES outputs without a source signal present will always be on but the signal will be silence.

3.1.4.7 !20/24 bit (SD), SW3.8
SD video will contain embedded audio with a word length of 24 bits if SW3.8 is on.

SD video will contain embedded audio with a word length of 20 bits if SW3.8 is off.

HD video will always contain embedded audio with a word length of 24 bits.

3.2 GYDA Control

Full control of the stereo audio router is possible with the GYDA system controller. Direct control with SNMP is available.

The module stores its routing configuration in non-volatile memory when a GYDA command is given. This allows complex configurations to be restored after a power loss.

The configuration is stored both in the card and in GYDA. If the card must be replaced, then GYDA will restore the configuration into the new card as long as SW1.8 is off.

If GYDA fails and there is a power glitch, then the card will reboot with the stored settings.

> If a GYDA system controller is present, the last configuration of the module will be only be restored by GYDA if SW1.8 is off. The intention is that SW1.8 is used to show that the card is manually configured when switched on.

3.2.1 Audio delay lines
The unit has four stereo audio delay lines connected to the audio router. Audio to be delayed is routed to one of the delay inputs and the output of that delay is routed to the intended output. The length of each delay line is set up on the configuration page of GYDA. The maximum delay for each of the four delays is 16384 audio samples, which is about 340ms. The delay lines may be cascaded if longer delays are required.

3.2.2 Stereo audio processing
The output of each stereo signal may be manipulated (LL, RR, LR, RL ØLR, LØR, L+R/2, MS) this is controlled with the GYDA controller.

The stereo signals may be output in one of the following ways:

    − LR,   Left / Right   No change.

- RL,   Right/ Left   Channels are swapped.
- LL,   Left/ Left   Left channel is copied into the right channel.
- RR,   Right/ Right   Right channel is copied into the left channel.
- ØLR,   ØLeft/ Right   The left channel is phase inverted.
- LØR,   Left/ ØRight   The right channel is phase inverted.
- L+R/2 Left + Right   The left and right channels are summed.
- MS   MS/AB   The left and right channels are converted from AB stereo to MS stereo.

The sum products (L+R/2 and MS) are reduced in level by 6 dB to avoid any possibility of clipping.

3.2.3 RS422 Data port configuration

The RS422 data 8P8C Modular jack input must be configured with GYDA. The baud rate, data length, parity and stop bits must be configured if UART mode is used.

The router destination where the data is to be embedded must be set up and the source channel containing the received data that will be output on the 8P8C Modular jack must be also be configured.

See further Chapter 3.3.

3.2.4 Transport and shuffling of audio groups

The AAV-HD-XMUX stereo *audio router* involves de-embedding, buffering and re-embedding which introduces a small delay relative to the video signal.

The *group router* is used to avoid this extra delay. Groups that only pass through the *group router* are re-embedded in the same video line. This avoids any extra delay and means that incompatible audio formats (asynchronous audio) may still be transported. The AAV-HD-XMUX automatically uses the *group router* whenever possible when controlled with the DIP switches.

"Shuffling" of groups is when existing embedded audio groups are re-assigned to different groups. Copying of groups is also possible i.e. Group 1 may be transported to Group 1 and duplicated to Group 2. This function also takes place in the *group router* which means that there is no extra delay.

3.2.5 Audio generator

The stereo audio generator is available in the audio router as a source. It is a high purity 1 kHz sine wave with a 250ms interruption on the left channel every 3 seconds. The audio level may be set to one of two standards. The two levels are -18 dBFS and -20 dBFS. These two levels correspond to EBU R68 and SMPTE RP 155.

3.2.6 Video generator

The video generator has several different simple signals:

- Color bar, 100% white, 75% colors, no set-up level.
- Red, Green, Blue or Black full field.

The generator may be used as a backup video source if there is no video signal present at either of the video inputs. The generator may also be switched on with GYDA. This will override video input but the generator signal will be locked to the input.

The video standard of the generator may be set with GYDA but only if there is no video input present.

3.2.7 Video input switching

The default mode of operation is auto-switch between the optical and electrical inputs. The video output may be configured to either use the internal generator, or to switch off when no video is detected on the inputs. The card will use the internal generator while it switches between inputs until it finds a valid video signal.

There is also a manual input mode which disables the input automatic switching between inputs. The video generator may be also selected to override the input video picture. The input video will decide the timing of the output video and any embedded packets will still be used by the module. Only the picture will be overwritten. If the video signal is removed however, the input will not switch even if there is a valid signal at the other input.

3.2.8 De-glitching

Upstream video switching in a router causes glitches in the digital video. The module will remove these glitches if the switch occurs on the correct video line for the standard in use. The input buffer is two video lines of the longest standard and starts in the middle. Subsequent switches will be transparent if the new signal phase is within a line from the original video. There will be a glitch on the output if the new video phase is outside of this range. The buffer will be re-aligned to the middle with the new signal phase.

3.3 Data transmission

The module can de-embed and embed asynchronous data. An AES3 audio signal is used as a carrier. Both embedded audio and normal AES3 signals may be used to carry the RS422 data. The fiber connection usually only goes one direction so any desired return path must be created by the user with another circuit. Return data may be sent over fiber via a link comprising of AV-HD-XMUX, AAV-HD-XMUX, AAV-HD-DMUX, AES, D422 or D422-MG modules.

The data input on the back panel works in one of two modes:

1. UART Mode: The data is checked for correct reception according to the configuration. The data words are packaged and sent when present.
2. Raw sampling mode: The data input is sampled at 93.75 kHz and embedded as a data stream. No checking is performed.

3.3.1 Data latencies

The data channel has a total latency of approximately 30µs when using raw sampling. Normal data rates of up to 9600 may be used with raw data sampling to have a low latency. The latency is 500µs when using the normal data encoding due to the block structure of the AES User bits.

The configuration of the data channel is always stored in the module and used regardless of the GYDA override switch.

3.3.2 Embedding

The AAV-HD-XMUX has a RS422 data input for the embedding of control data. The baud rate and other parameters are configured with GYDA. The factory default is 115200 baud, no parity, one stop bit.

The data channel is encoded in the User bits in an embedded audio stereo signal assigned with GYDA. The factory default is Audio channels 1&2 in Group 1.

The data is sampled asynchronously at a constant bit rate. The range of baud rates that may be transmitted is from DC to 115,200 bps. The data bytes are either encoded as packets in the transmitted data or transmitted as an asynchronous bit stream which may also be used to transmit a DC signal such as GPI.

3.3.3 De-embedding

The audio channel with the data signal to be de-embedded must be configured by GYDA as there may be several data channels available.

The AAV-HD-XMUX will automatically detect the data channel format when present and output the data on the 8P8C Modular jack connector. The output driver will only be active when data is output in UART mode. The means that the output is always active when raw data is used.

3.3.4 Limitations

1. There is one thing the user must do in order to receive embedded data. The audio source where the data is embedded must be routed to a destination in the stereo router. This is because the extraction of the data takes place on the output of the router.
   Example: Data is to be de-embedded from embedded audio channels 1&2. Route embedded audio channels 1&2 routed to output to Delay 4.

2. The normal UART mode checks the data when receiving and only embeds valid bytes. The data format must be correct. This also means that a BREAK condition of many spaces will not be detected or transmitted. Contact support if this is a requirement.

4 Connections

Two backplanes are presently available for the AAV-HD-XMUX.
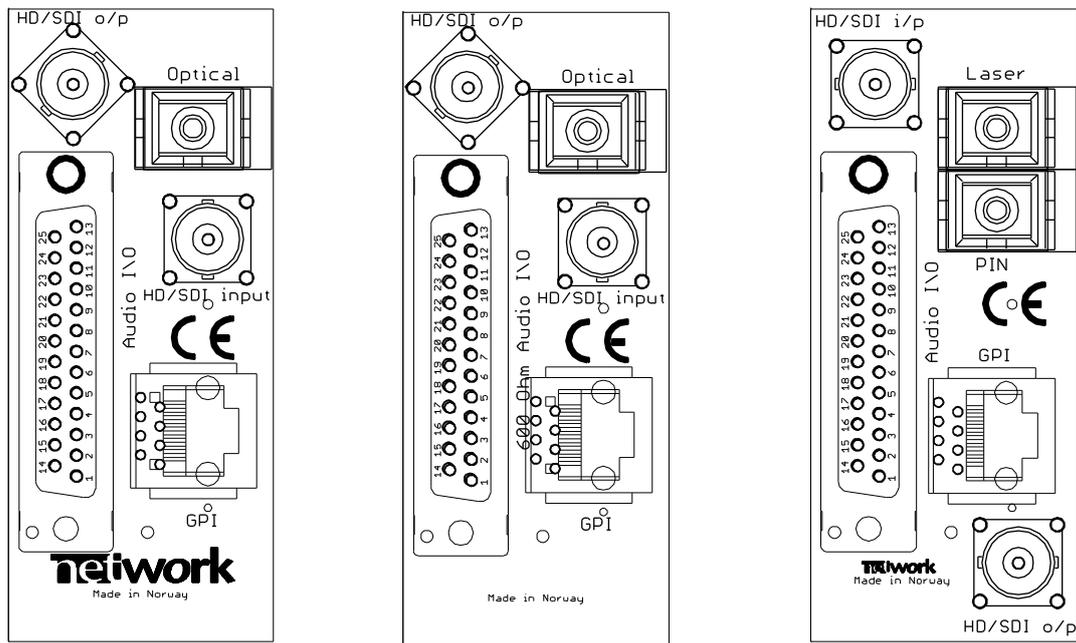
Figure 5: AAV-DMUX-C1 (left) AAV-HD-XMUX-C6 and AV-HD-XMUX-C1 (right)

The AAV-DMUX-C1 is the standard backplane / connector module with BNC video electrical inputs and outputs. A DB-25 type connector is provided for the analog and AES3 audio inputs and outputs. The pin configuration used is the industry standard DA-88 type so that commercially available 'snakes' may be used.

The AAV-HD-DMUX-C6 is a backplane with termination resistors for the analogue inputs to allow the module to be used with 600 ohm lines. This module should only be used when there is requirement for a matched impedance as it also introduces a 6 dB level reduction.

The AV-HD-XMUX-C1 is a backplane / connector module with two optical ports and a BNC video electrical input and output. A DB-25 connector is provided for the audio inputs and outputs.
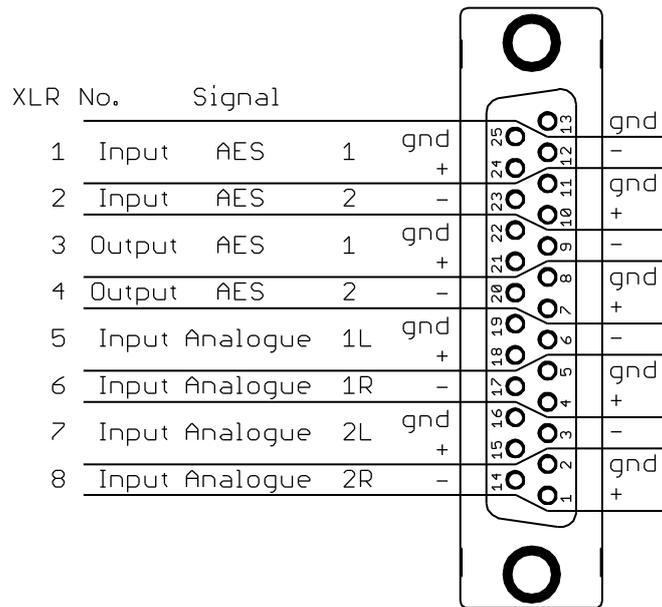
4.1 Audio connections DB-25

Figure 6: DB-25 audio connector wiring

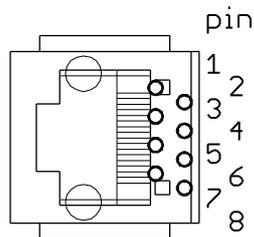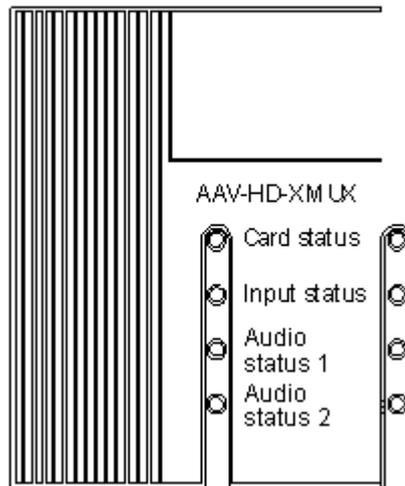## 4.2 GPI/ Data connections 8P8C Modular jack

Figure 7: Pin layout 8P8C Modular jack

Pin number	Description
1	Power present
2	No Video signal
3	Laser failure
4	RS485/422 output +
5	RS485/422 output -
6	RS422 input +
7	RS422 input -
8	Ground

5 Operation

Figure 8: LED overview

(Text not printed on the front panel). Each module has 4 LED's. The colors of each of the LED's have different meanings as shown in the tables below.

5.1 Front panel LEDs

AES status LEDS show information decided by DIP switch 2.7- LED mode.

'On' corresponds to the AV-MUX/ AV-HD-XMUX audio input mode.

'Off' corresponds to the AV-DMUX/ AAV-DMUX de-embedder audio mode.

Diode \ state	Red LED	Orange LED	Green LED	No light
Card status	PTC fuse has been triggered or FPGA programming has failed or laser has failed	Module has not been programmed	Module is OK	Module has no power
SDI input status	Video signal absent.	Electrical video signal present	Optical video signal Present	Module has not been programmed
Audio status 1: Switch 2.7 off	na	na	Always on (A/D converter is always on)	Module has not been programmed
Audio status 2: Switch 2.7 off	AES input 1 & 2 not present	Either AES input 1 or 2 present	AES input 1 & 2 both present	Module has not been programmed
Audio status 1: Switch 2.7 on	Group 1 & 2 not present	Either group 1 or 2 present	Group 1 & 2 both present	Module has not been programmed
Audio status 2: Switch 2.7 on	Group 3 & 4 not present	Either group 3 or 4 present	Group 3 & 4 both present	Module has not been programmed

5.2 GPI alarms

Only three alarms are present on the 8P8C Modular jack connector as four of the pins are used for the RS422 data port.

The three alarms are:

- Power present (negative logic)
- Video signal lost
- Laser failure

An active alarm condition means that the transistor is conducting.

The power present alarm should always be active during normal operation.

6 Laser safety precautions

These are guidelines to limit hazards from laser exposure.

All the available EO and –T units in the Flashlink range include a laser.

Therefore this note on laser safety should be read thoroughly.

The lasers emit light at wavelengths from 1270nm up to 1610nm. This means that the human eye cannot see the beam, and the blink reflex cannot protect the eye. (The human eye can see light between 400 nm to 700 nm).

A laser beam can be harmful to the human eye (depending on laser power and exposure time). Therefore:

> Be careful when connecting / disconnecting fiber pigtails (ends).
>
> Never look directly into the pigtail of the laser/fiber.
>
> Never use microscopes, magnifying glasses or eye loupes to look into a fiber end.
>
> Use laser safety goggles blocking light at 1310 nm and at 1550 nm

Instruments exist to verify light output power: Power meters, IR-cards etc.
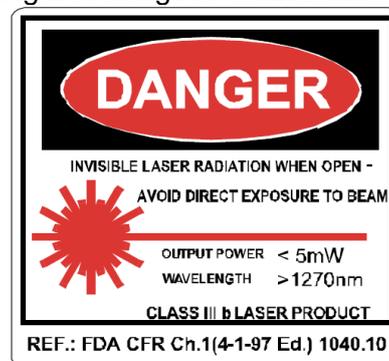
Flashlink features:

All the laser module cards in the Flashlink product range, are Class 1 laser products according to IEC 825-1 1993, and class I according to 21 CFR 1040.10 when used in normal operation.

More details can be found in the user manual for the FR-2RU-10-2 frame.

Maximum output power[3]: 5 mW   Operating wavelengths: > 1270 nm

CLASS 1
LASER PRODUCT

DANGER

INVISIBLE LASER RADIATION WHEN OPEN -
AVOID DIRECT EXPOSURE TO BEAM

OUTPUT POWER   < 5mW
WAVELENGTH   >1270nm

CLASS III b LASER PRODUCT

REF.: FDA CFR Ch.1(4-1-97 Ed.) 1040.10

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[3] Max power is for safety analysis only and does not represent device performance.

General environmental requirements for Nevion equipment

1. The equipment will meet the guaranteed performance specification under the following environmental conditions:
    - Operating room temperature range: 0°C to 45°C
    - Operating relative humidity range: <90% (non-condensing)

2. The equipment will operate without damage under the following environmental conditions:
    - Temperature range: -10°C to 55°C
    - Relative humidity range: <95% (non-condensing)

Product Warranty

The warranty terms and conditions for the product(s) covered by this manual follow the General Sales Conditions by Nevion, which are available on the company web site:

www.nevion.com

Appendix A Materials declaration and recycling information

A.1 Materials declaration

For product sold into China after 1st March 2007, we comply with the "Administrative Measure on the Control of Pollution by Electronic Information Products". In the first stage of this legislation, content of six hazardous materials has to be declared. The table below shows the required information.

組成名稱 Part Name	Toxic or hazardous substances and elements					
	鉛 Lead (Pb)	汞 Mercury (Hg)	镉 Cadmium (Cd)	六价铬 Hexavalent Chromium (Cr(VI))	多溴联苯 Polybrominated biphenyls (PBB)	多溴二苯醚 Polybrominated diphenyl ethers (PBDE)
AAV-HD-XMUX(-T/R)/ AAV-SD-XMUX(-T/R)	O	O	O	O	O	O
O: Indicates that this toxic or hazardous substance contained in all of the homogeneous materials for this part is below the limit requirement in SJ/T11363-2006. X: Indicates that this toxic or hazardous substance contained in at least one of the homogeneous materials used for this part is above the limit requirement in SJ/T11363-2006.						

This is indicated by the product marking:

A.2 Recycling information

Nevion provides assistance to customers and recyclers through our web site http://www.nevion.com. Please contact Nevion's Customer Support for assistance with recycling if this site does not show the information you require.

Where it is not possible to return the product to Nevion or its agents for recycling, the following general information may be of assistance:

- Before attempting disassembly, ensure the product is completely disconnected from power and signal connections.
- All major parts are marked or labeled to show their material content.
- Depending on the date of manufacture, this product may contain lead in solder.
- Some circuit boards may contain battery-backed memory device.